IN THE CLAIMS:

Claims 1-10 are pending.

Claims 1-6 are amended herein.

Claims 7-10 are cancelled herein.

Claims 11-13 are newly added herein.

The status of the claims is as follows:

Claim 1 (currently amended) A memory interlace-checking method to detect weakened memory in a memory array composed of odd and even addresses, ~~which comprises~~ the method comprising:

sequentially performing accessing commands on the odd addresses in the memory array; and

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sequentially performing data checking commands on the even addresses in the memory array that are complementary to the odd addresses.

~~a main step, which has at least one main address accessing datum and commands to perform actions on each memory addresses;~~

~~a data checking step, which includes an address accessing datum containing data checking commands that check data in part of the memory addresses complementary to the main address accessing datum.~~

Claim 2 (Currently amended) The method of claim 1, wherein the odd and even addresses are memory rows. ~~main step performs command actions on interlacing memory columns.~~

Claim 3 (Currently amended) The method of claim 1, wherein the odd and even addresses are memory columns. ~~main step performs command actions on interlacing memory columns.~~

Claim 4 (Currently amended) A memory interlace-checking method to detect weakened memory, the method comprising:

executing a test program with command actions, wherein the testing program has:

~~which is implemented in a test program with a command action, the test program comprising:~~

at least a portion of main address accessing data; and

at least a portion of secondary address accessing data; which is at least partially complementary to the portion of main address accessing data.

Claim 5 (Currently amended) The method of claim 4, wherein the main address accessing data contains the command actions.

Claim 6 (Currently amended) The method of claim 4, wherein the secondary address accessing data contains [[a]] checking actions.

Claims 7-10 are cancelled herein.

Claim 11 (New) A memory interlace-checking method to detect weakened memory in a memory array composed of odd and even addresses, the method comprising:

sequentially performing accessing commands on the even addresses in the memory array; and

sequentially performing data checking commands on the odd addresses in the memory array that are complementary to the even addresses.

Claim 12 (New) The method of claim 11, wherein the odd and even addresses are memory rows.

Claim 13 (New) The method of claim 11, wherein the odd and even addresses are memory columns.